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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/677,350	10/02/2003	2811	385	5051-531DV	3	7	1

CONFIRMATION NO. 9811

20792
MYERS BIGEL SIBLEY & SAJOVEC
PO BOX 37428
RALEIGH, NC 27627

FILING RECEIPT



OC000000011632638

01-09-04 A19:35 IM

Date Mailed: 01/07/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Zhibo Zhang, Raleigh, NC;

Domestic Priority data as claimed by applicant

This application is a DIV of 10/007,895 11/06/2001 PAT 6,664,143, which itself claims
benefit of 60/252,306, 11/22/2000

Foreign Applications

if Required, Foreign Filing License Granted: 01/06/2004

Projected Publication Date: 04/15/2004

Non-Publication Request: No

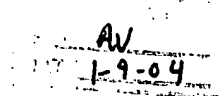
Early Publication Request: No

** SMALL ENTITY **

Title

Vertical field effect transistors including conformal monocrystalline silicon layer on trench sidewall

Preliminary Class



**LICENSE FOR FOREIGN FILING UNDER
Title 35, United States Code, Section 184
Title 37, Code of Federal Regulations, 5.11 & 5.15**

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Attorney Docket No. 5051-531DV

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: Zhibo Zhang
Serial No.: 10/677,350
Filed: October 2, 2003
For: VERTICAL FIELD EFFECT TRANSISTORS INCLUDING CONFORMAL
MONOCRYSTALLINE SILICON LAYER ON TRENCH SIDEWALL

Confirmation No.: 9811
Examiner: Dao H. Nguyen
Group Art Unit: 2818

April 15, 2004

Mail Stop Non-Fee Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

SECOND REQUEST FOR CORRECTED FILING RECEIPT

Sir:

This second request is responsive to the "Response to Request for Corrected Filing Receipt" mailed January 29, 2004. In this Response, the PTO indicated that it could not comply with Applicant's request, because the provisional application to which priority was claimed was filed over a year ago. However, the present application is a divisional of Application Serial No. 10/007,985, filed 11/06/2001, now U.S. Patent 6,664,143, which itself claimed benefit of provisional Application Serial No. 60/252,306, filed 11/22/2000. Accordingly, Applicant's claim for domestic priority is correct. A redline copy of the official Filing Receipt is attached. As noted, the domestic priority information should read as follows:

This application is a DIV of 10/007,895 11/06/2001 PAT 6,664,143 which itself claims benefit of 60/252,306 11/22/2000.

Applicant respectfully requests that the domestic priority information be corrected in the records of the U.S. Patent and Trademark Office and that a corrected Filing Receipt be issued.

No fee is believed due. However, the Commissioner is hereby authorized to charge any deficiency or credit any overpayment to Deposit Account No. 50-0220.

Respectfully submitted,

Mitchell S. Bigel
Registration No. 29,614

Customer Number 20792
Myers Bigel Sibley & Sajovec, P.A.
P.O. Box 37428, Raleigh, NC 27627
919-854-1400
919-854-1401 (Fax)

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Mail Stop Non-Fee Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on April 15, 2004.

Susan E. Freedman
Date of Signature: April 15, 2004